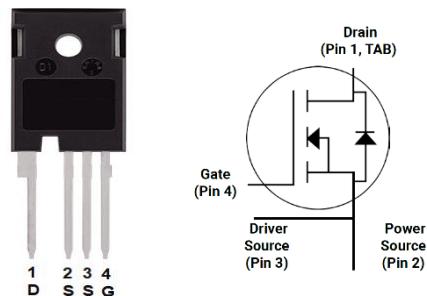


Product Summary

$V_{DS} = 1200 \text{ V}$
 $I_D @ 25^\circ\text{C} = 64\text{A}$
 $R_{DS(\text{ON})} = 36\text{m}\Omega$



TO-247-4

Features

- High Blocking Voltage
- High Frequency Operation
- Low on-resistance
- Fast intrinsic diode with low reverse recovery

Benefits

- Higher System Efficiency
- Parallel Device Convenience without thermal runaway
- High Temperature Application
- Hard Switching & Higher Reliability
- Easy to drive

Applications

- Motor Drives
- Solar / Wind Inverters
- EV Charging Station
- AC/DC converters
- DC/DC converters
- Uninterruptable power supplies

Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test conditions	Value	Unit
Drain - Source Voltage	$V_{DS\text{max}}$	$V_{GS}=0\text{V}, I_D=100\mu\text{A}$	1200	V
Gate - Source Voltage (dynamic)	$V_{GS\text{max}}$	AC ($f>1 \text{ Hz}$)	-10 / +25	V
Gate - Source Voltage (static)	$V_{GS\text{op}}$	static	-5 / +20	V
Continuous Drain Current	I_D	$V_{GS} = 20\text{V}, T_c=25^\circ\text{C}$ $V_{GS} = 20\text{V}, T_c=100^\circ\text{C}$	64 45	A
Pulsed Drain Current	$I_{D(\text{pulse})}$	$T_c=25^\circ\text{C}$	112	A
Short Circuit Capability	t_{SC}	$V_{DD}=800\text{V}, V_{GS}=20\text{V}$	3	μs
Short Circuit Capability	I_{DS}	$V_{DD}=800\text{V}, V_{GS}=20\text{V}$	600	A
Total power dissipation	P_D	$T_c=25^\circ\text{C}$	300	W
Operating Junction Temperature	T_J		-55 to 175	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 to 175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 100\mu\text{A}$	1200			V	
Gate Threshold Voltage		$V_{DS} = V_{GS}, I_D = 10\text{mA}$	1.8	2.8	3.9	V	
		$V_{DS} = V_{GS}, I_D = 10\text{mA}, T_J = 150^\circ\text{C}$		1.9			
		$V_{DS} = V_{GS}, I_D = 10\text{mA}, T_J = 175^\circ\text{C}$		1.8		V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 1200\text{V}, V_{GS} = 0\text{V}$	0	5	100	μA	
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = 20\text{V}, V_{DS} = 0\text{V}$	0	10	200	nA	
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = -5\text{V}, V_{DS} = 0\text{V}$	-200	-10	0	nA	
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{GS} = 18\text{V}, I_D = 40\text{ A}$		41		$\text{m}\Omega$	
		$V_{GS} = 18\text{V}, I_D = 40\text{ A}, T_J = 150^\circ\text{C}$		61			
		$V_{GS} = 18\text{V}, I_D = 40\text{ A}, T_J = 175^\circ\text{C}$		68			
		$V_{GS} = 20\text{V}, I_D = 40\text{ A}$		36	50		
		$V_{GS} = 20\text{V}, I_D = 40\text{ A}, T_J = 150^\circ\text{C}$		58			
		$V_{GS} = 20\text{V}, I_D = 40\text{ A}, T_J = 175^\circ\text{C}$		65			
Transconductance	g_{fs}	$V_{DS} = 20\text{V}, I_D = 40\text{ A},$		23		S	
		$V_{DS} = 20\text{V}, I_D = 40\text{ A}, T_J = 150^\circ\text{C}$		20			
		$V_{DS} = 20\text{V}, I_D = 40\text{ A}, T_J = 175^\circ\text{C}$		20			
Input capacitance	C_{iss}	$V_{DS} = 1000\text{V}, V_{GS} = 0\text{V}$ $f = 1\text{MHz}$		2980		pF	
Output capacitance	C_{oss}			143			
Reverse transfer capacitance	C_{rss}			15			
C_{oss} Stored Energy	E_{oss}			92			
Total gate charge	Q_g	$V_{DS} = 800\text{V}, V_{GS} = -5\text{V} / 20\text{V}$ $I_D = 40\text{ A},$		148		nC	
Gate-source charge	Q_{gs}			40			
Gate-drain charge	Q_{gd}			70			
Internal gate input resistance	$R_{\text{g(int)}}$	$f = 1\text{MHz}, I_D = 0\text{A}$		2		Ω	
Turn-On Switching Energy	E_{ON}	$V_{DS} = 800\text{ V}, V_{GS} = -5\text{V}/20\text{V},$ $I_D = 40\text{A}, R_{\text{G(ext)}} = 2\Omega,$ $L = 200\mu\text{H}$		240		μJ	
Turn-Off Switching Energy	E_{OFF}			80			
Turn-On Delay Time	$t_{\text{d(on)}}$			12			
Rise Time	t_r			16			
Turn-Off Delay Time	$t_{\text{d(off)}}$			27			
Fall Time	t_f			7			
Avalanche Capability	E_{AS}	$V_{DD} = 100\text{V}, V_{GS}=20\text{V}, L=2\text{mH}$		400		mJ	
Avalanche Capability	I_{AV}	$V_{DD} = 100\text{V}, V_{GS}=20\text{V}, L=2\text{mH}$		20		A	

Reverse Diode Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Diode Forward Voltage	V_{SD}	$V_{GS} = -5V, I_{SD} = 20A,$		3.9		V
		$V_{GS} = -5V, I_{SD} = 20A, T_J = 150^\circ\text{C}$		3.7		
		$V_{GS} = -5V, I_{SD} = 20A, T_J = 175^\circ\text{C}$		3.6		
Continuous Diode Forward Current	I_S	$V_{GS} = -5V$		60		A
Reverse Recovery time	t_{rr}	$V_{GS} = -5V, I_{SD} = 40A,$		17		ns
Reverse Recovery Charge	Q_{rr}			310		nC
Peak Reverse Recovery Current	I_{rrm}	$V_R = 800V, \text{dif/dt} = 4200 A/\mu\text{s}$		30		A

Thermal Characteristics

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Thermal Resistance (per device)	$R_{th(j-c)}$	junction-case		0.4	0.5	°C/W

Typical Performance

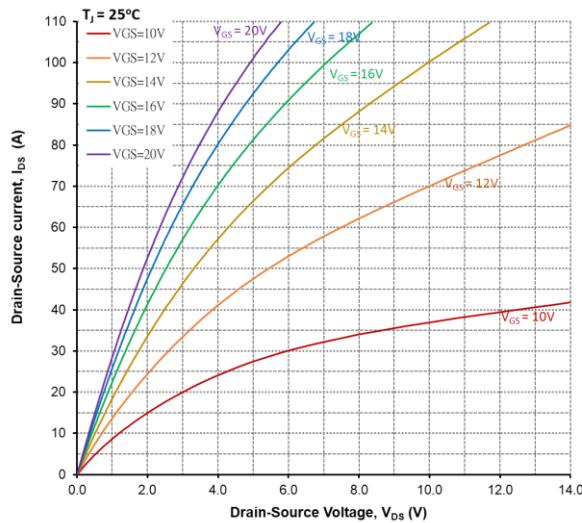


Figure 1. Output Characteristics, $T_J = 25^\circ\text{C}$

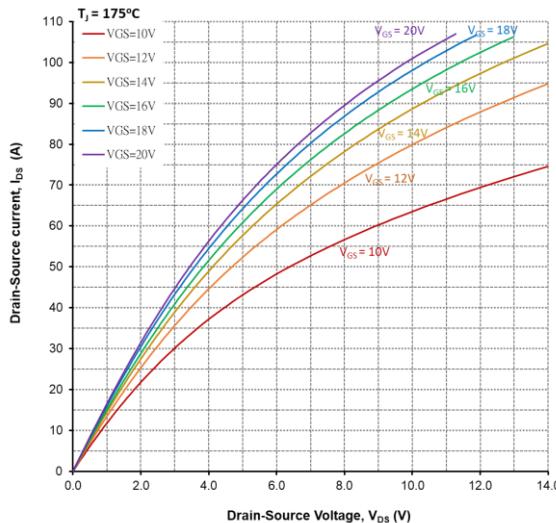


Figure 2. Output Characteristics, $T_J = 175^\circ\text{C}$

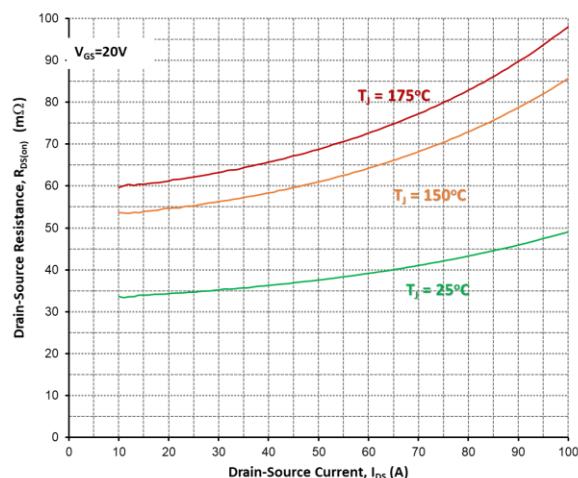


Figure 3. On-Resistance vs. Drain Current
For Various Temperatures

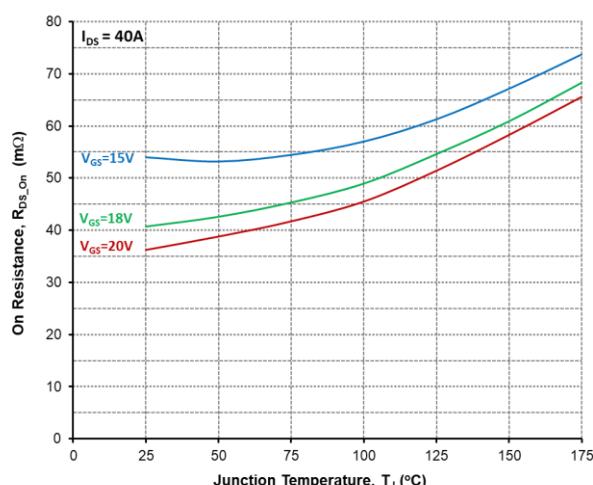


Figure 4. On-Resistance vs. Temperature

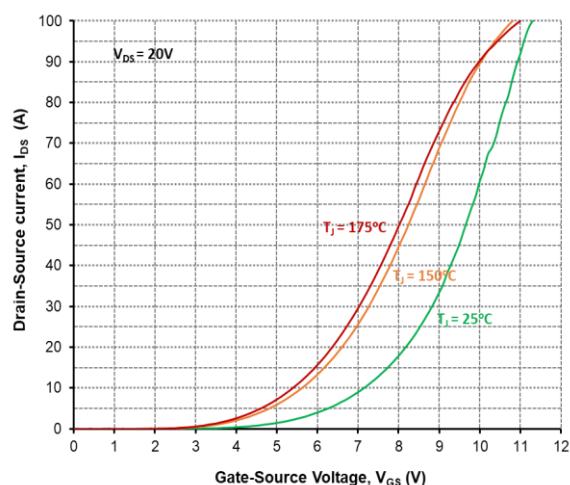


Figure 5. Transfer Characteristic For Various Junction Temperatures

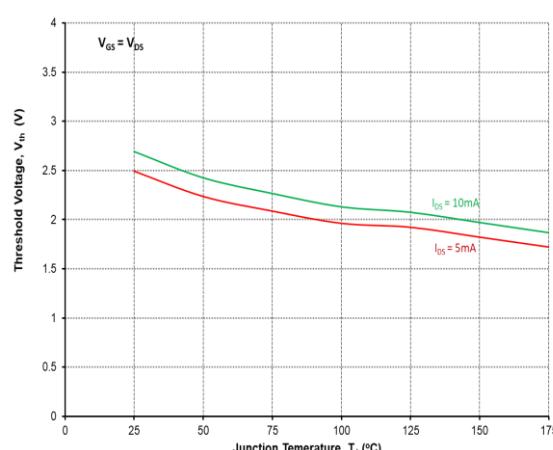


Figure 6. Threshold Voltage vs. Temperature

Typical Performance

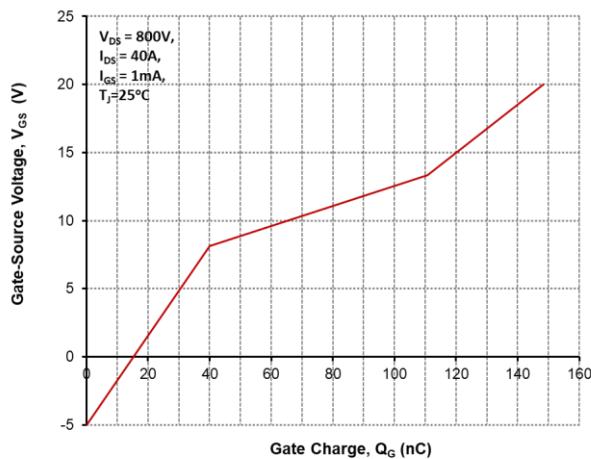


Figure 7. Gate Charge Characteristics

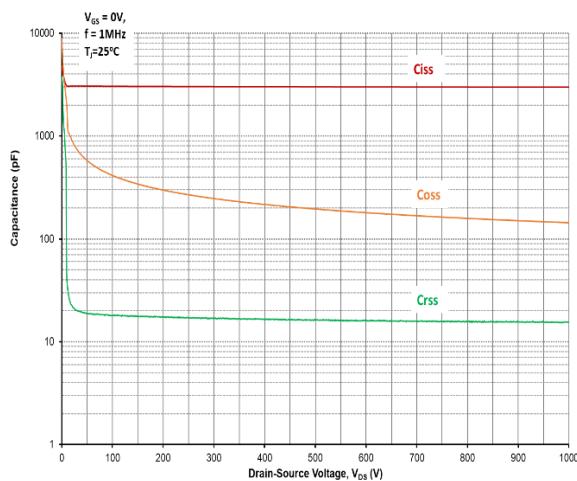


Figure 8. Capacitances vs. Drain-Source Voltage (0-1000V)

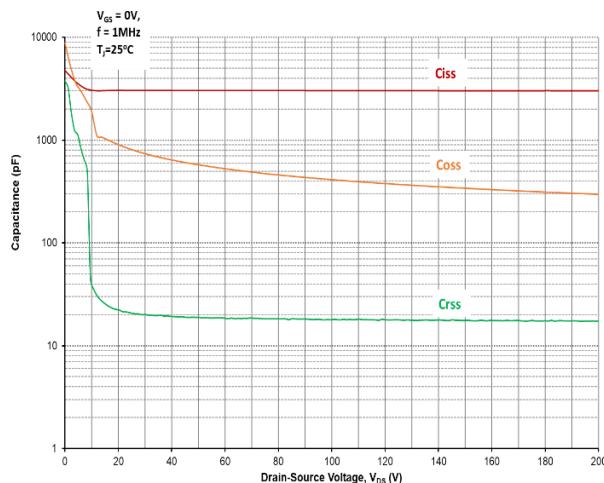


Figure 9. Capacitances vs. Drain-Source Voltage (0-200V)

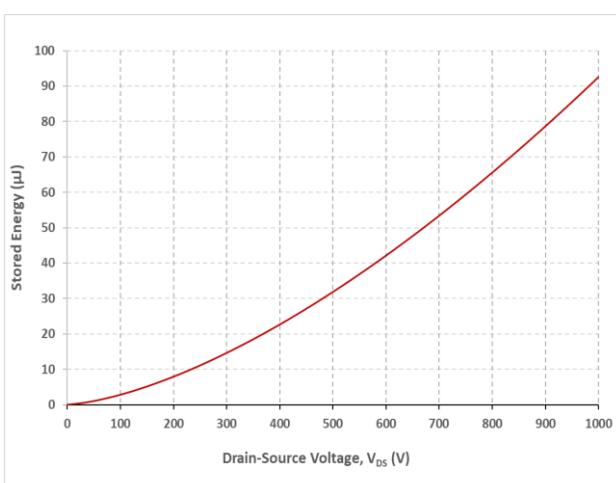


Figure 10. Output Capacitor Stored Energy

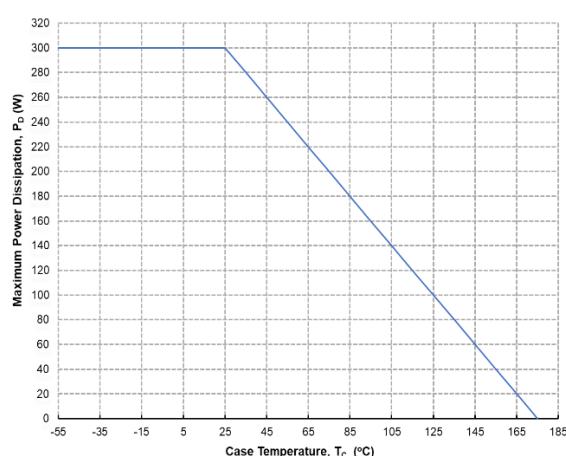


Figure 11. Maximum Power Dissipation Derating vs. Case Temperature

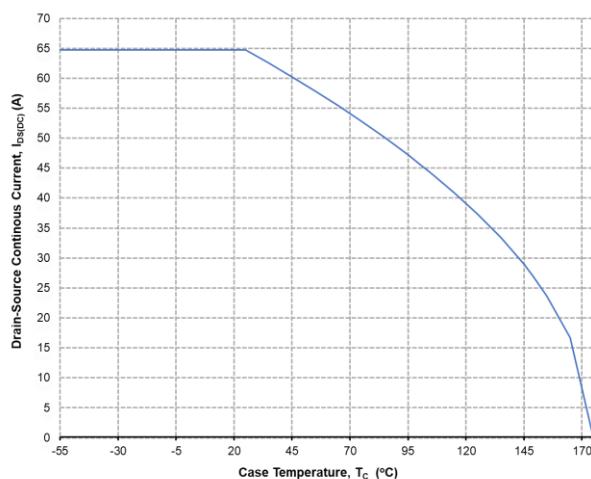


Figure 12. Continuous Drain Current Derating vs. Case Temperature

Typical Performance

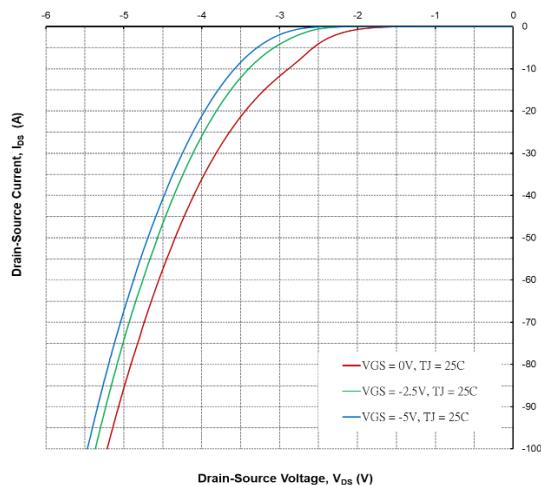


Figure 13. Body Diode Characteristics @ 25°C

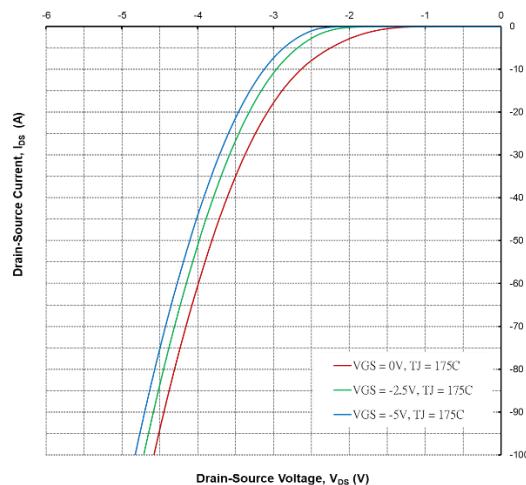


Figure 14. Body Diode Characteristics @ 175°C

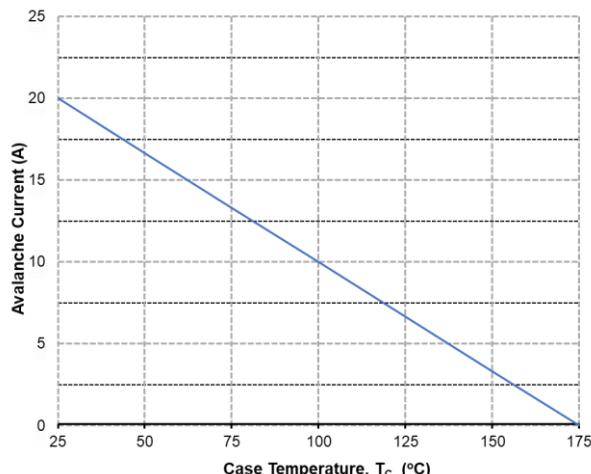


Figure 15. Single Avalanche vs. Temperature

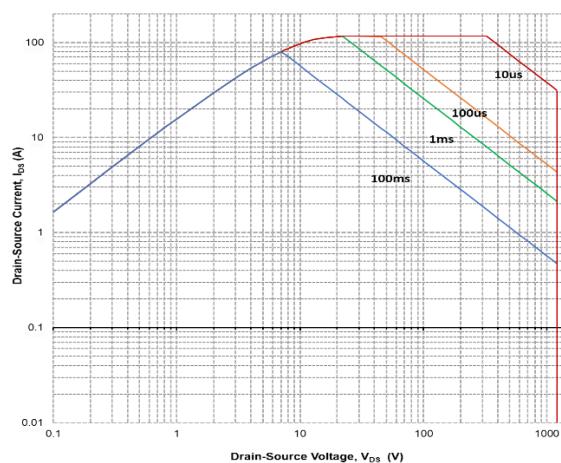
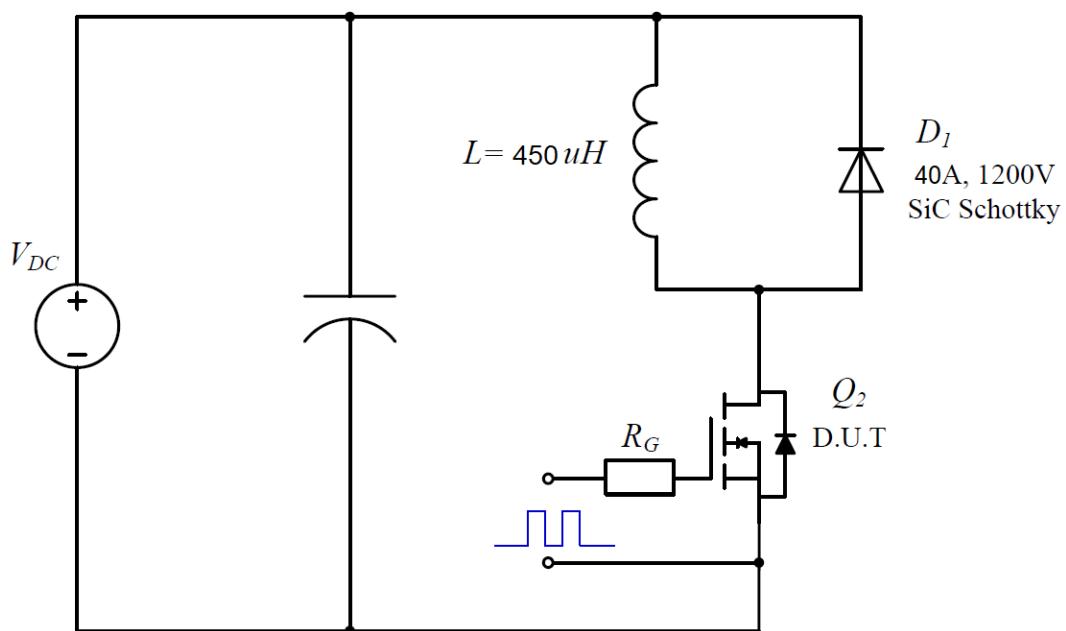
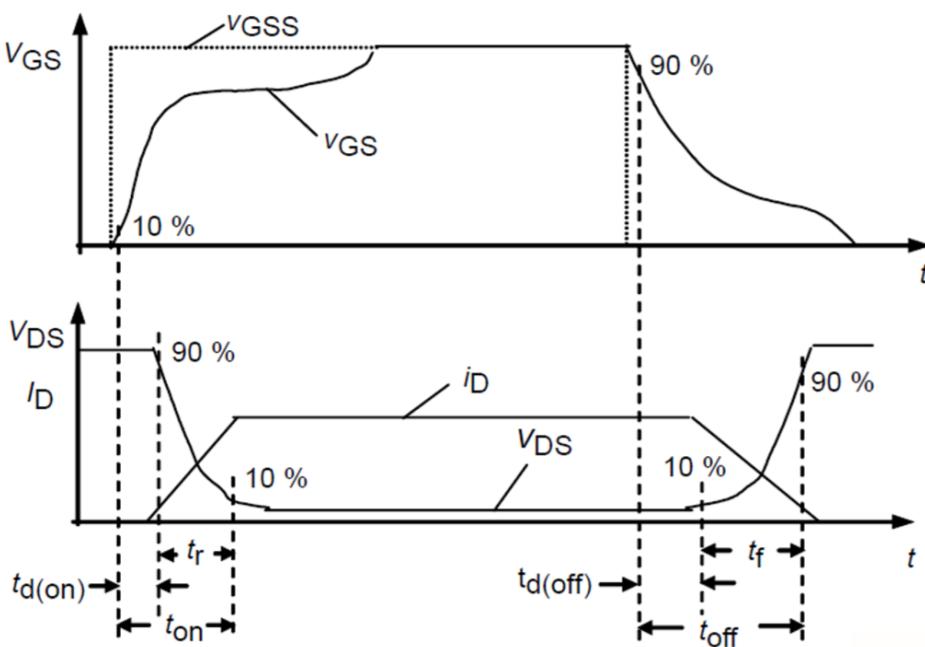


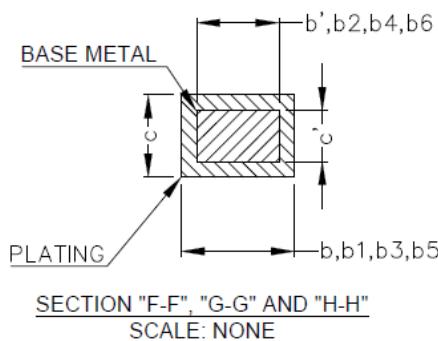
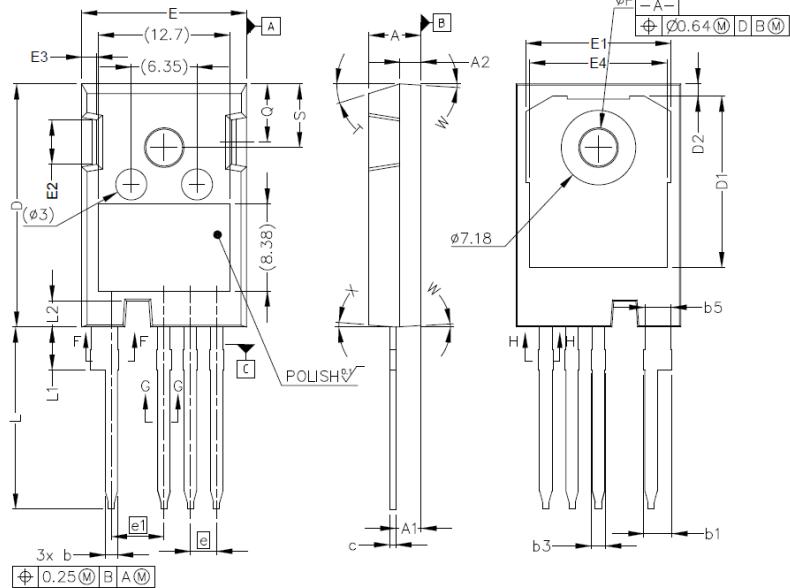
Figure 16. Safe Operating Area

Switching Times Definition and Test Circuit



Package Dimensions

(TO-247-4 Package)



SYMBOL	MILLIMETERS	
	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	2.39	2.94
b2	2.39	2.84
b3	1.07	1.60
b4	1.07	1.50
b5	2.39	2.69
b6	2.39	2.64
c'	0.55	0.65
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	2.54 BSC	
e1	5.08 BSC	
N	4	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
øP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF.	
W	3.5 ° REF.	
X	4° REF.	

NOTE :

- NOTE ,

 1. ALL METAL SURFACES: TIN PLATED,EXCEPT AREA OF CUT
 2. DIMENSIONING & TOLERANCEING CONFIRM TO ASME Y14.5M-1994.
 3. ALL DIMENSIONS ARE IN MILLIMETERS.
ANGLES ARE IN DEGREES.